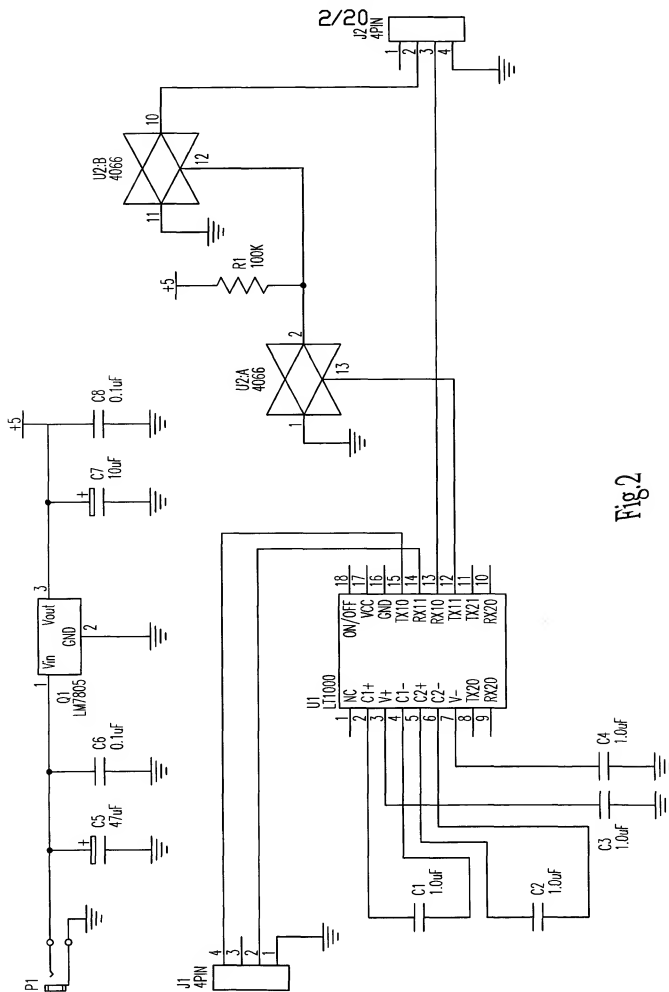


Fig. 1



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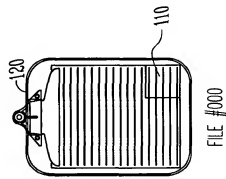
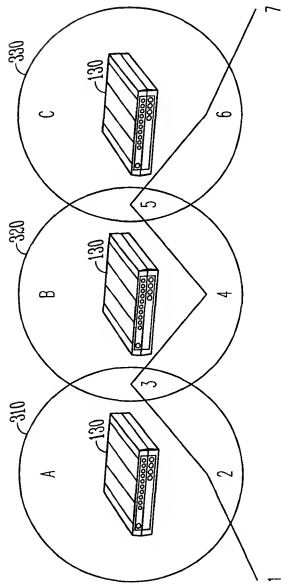


Fig.3

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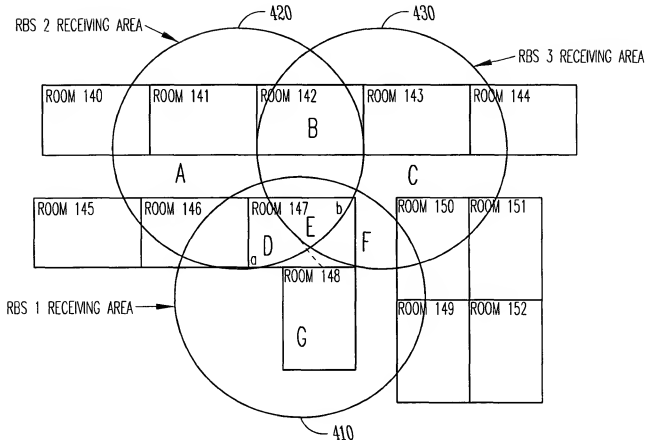


Fig.4

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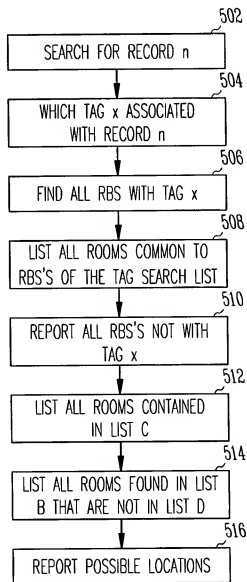


Fig. 5A

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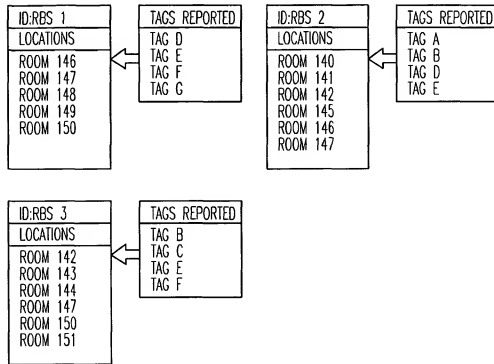


Fig.5B

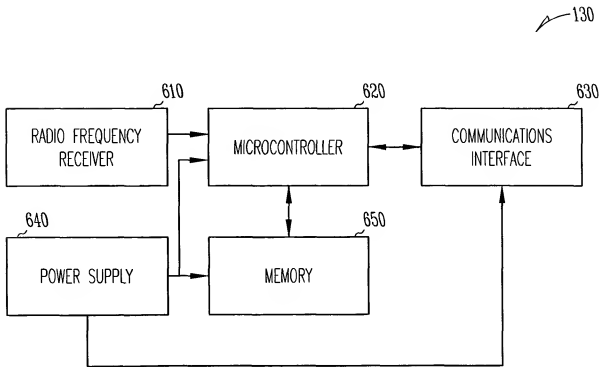
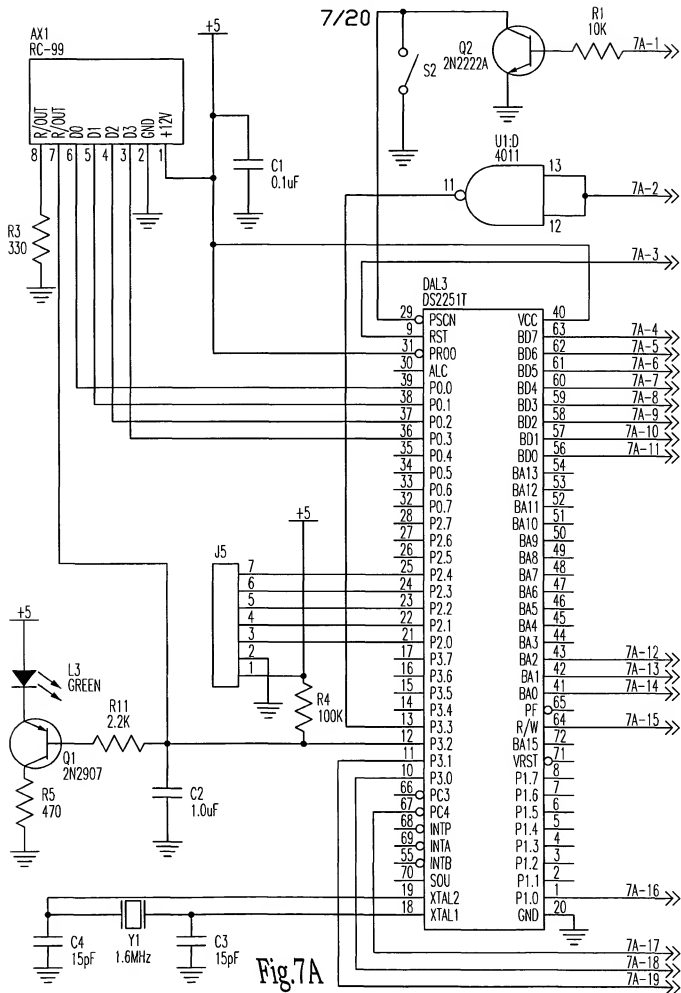
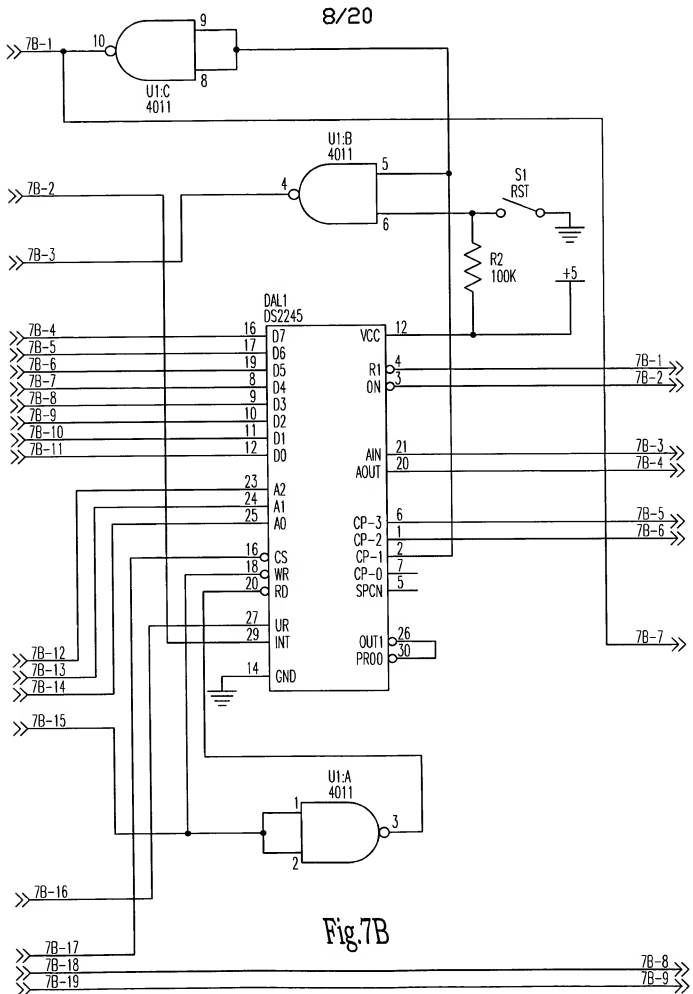


Fig.6





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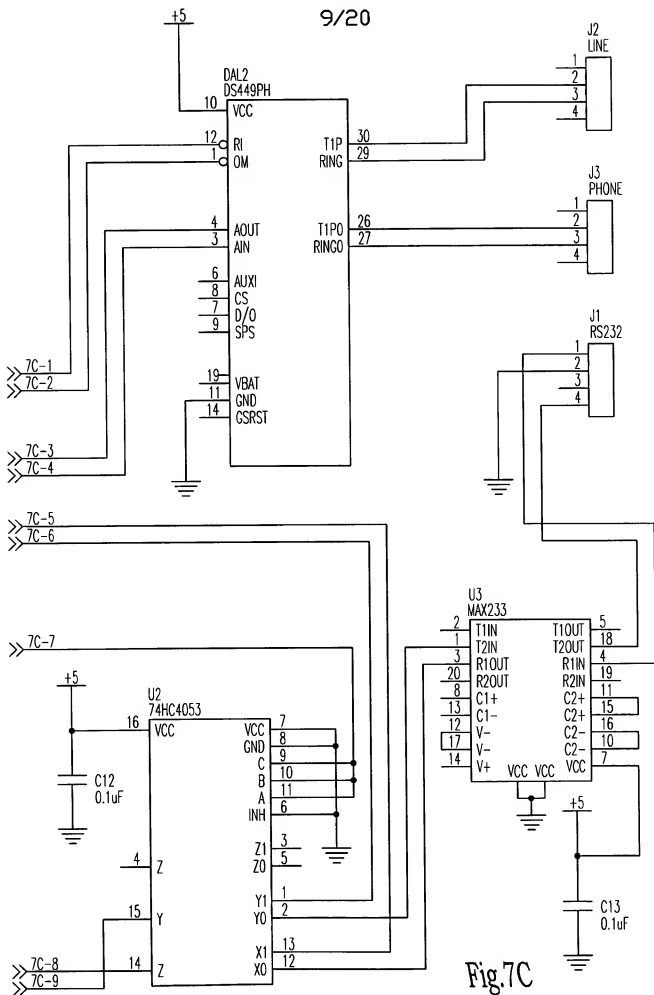


Fig.7C



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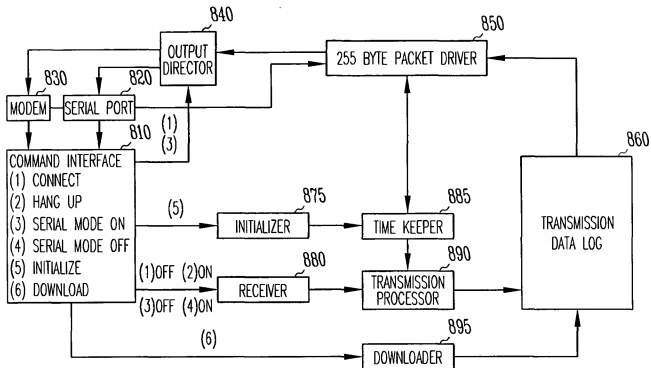


Fig.8

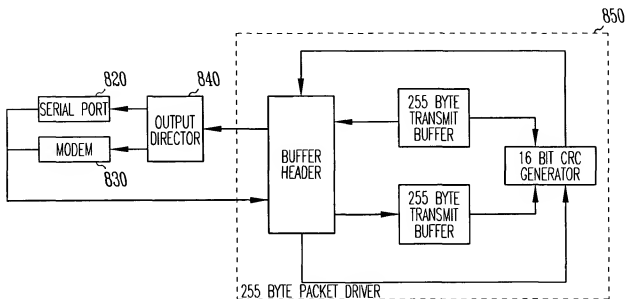
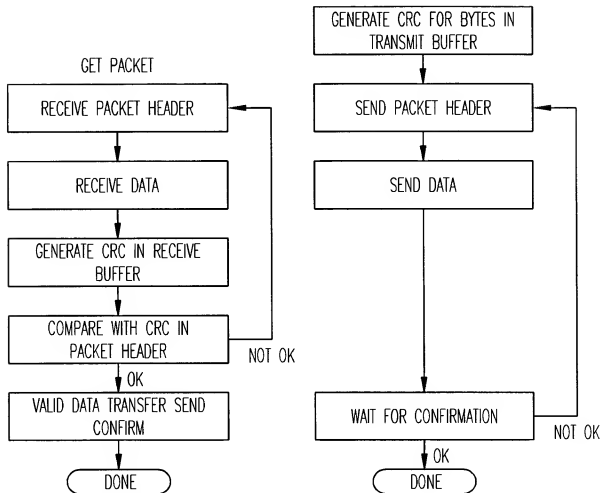


Fig.9

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PACKET HEADER

NUMBER OF BYTES TO RECEIVE/TRANSMIT

RECEIVED/GENERATED CRC

Fig.10

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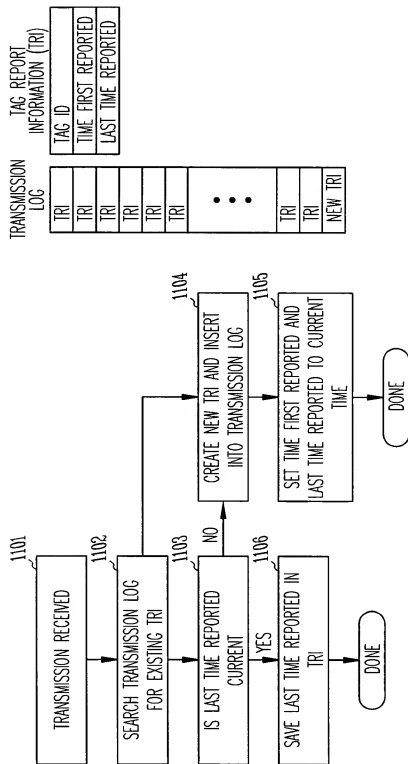


Fig.11

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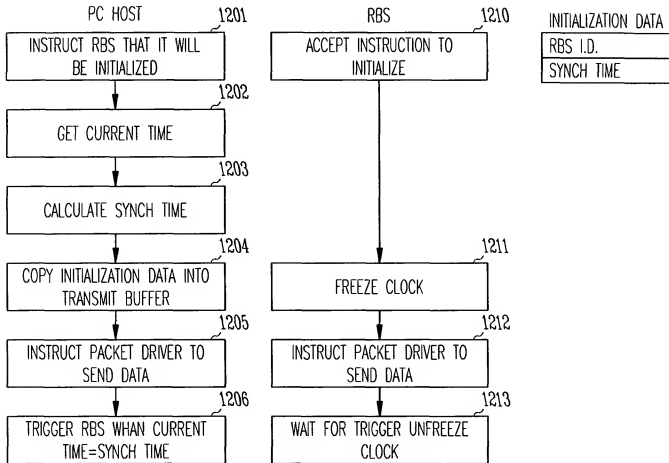


Fig.12

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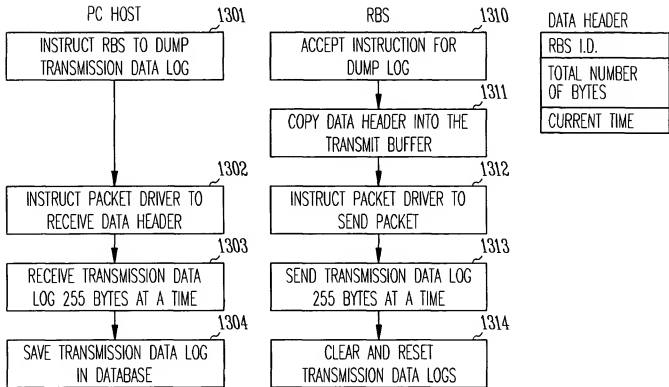


Fig.13

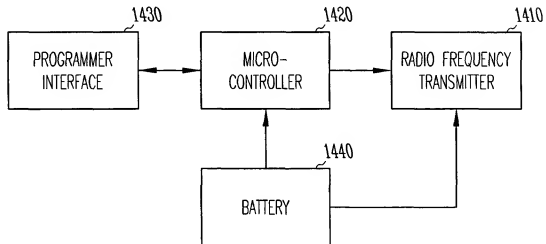


Fig.14

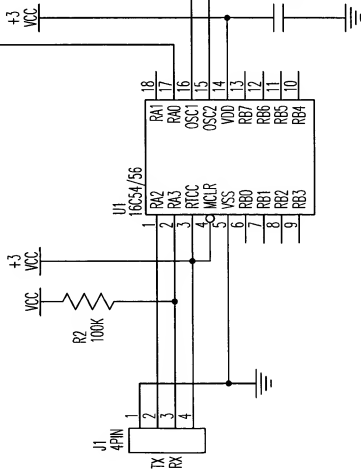


Fig. 15

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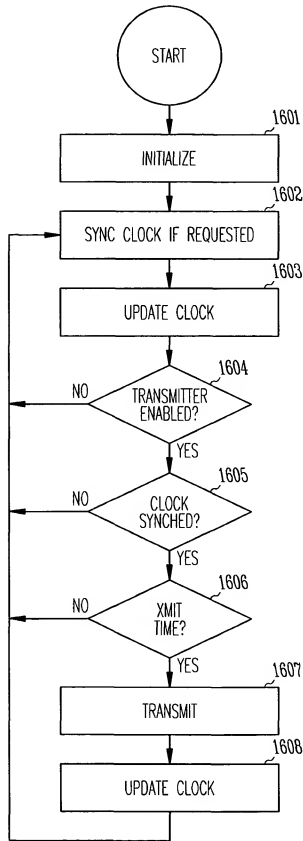


Fig.16

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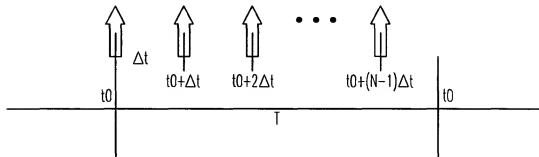
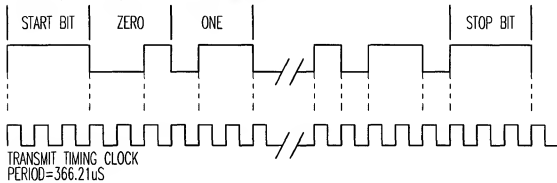


Fig.17

START BIT, STOP BIT, AND DATA BIT FORMAT



BYTE SEPERATION TIMING

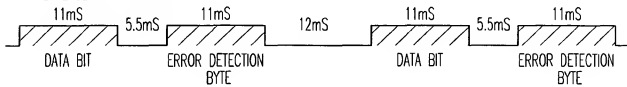


Fig.18

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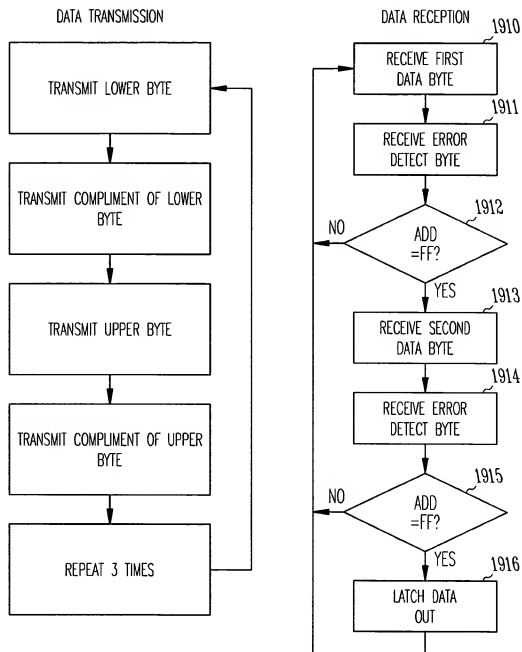
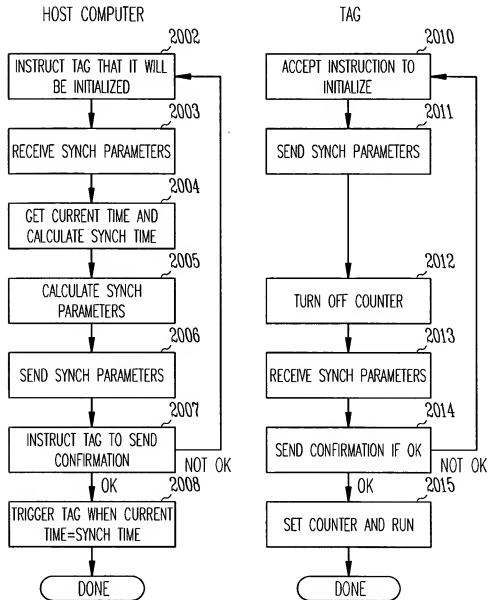


Fig.19

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SYNCH PARAMETERS

TAG ID	TIME CORRECTION COUNT
FRAME SIZE	TIME CORRECTION ADJUST VALUE
TRANSMISSION COUNT	COUNT SINCE LAST SYNCH

Fig.20